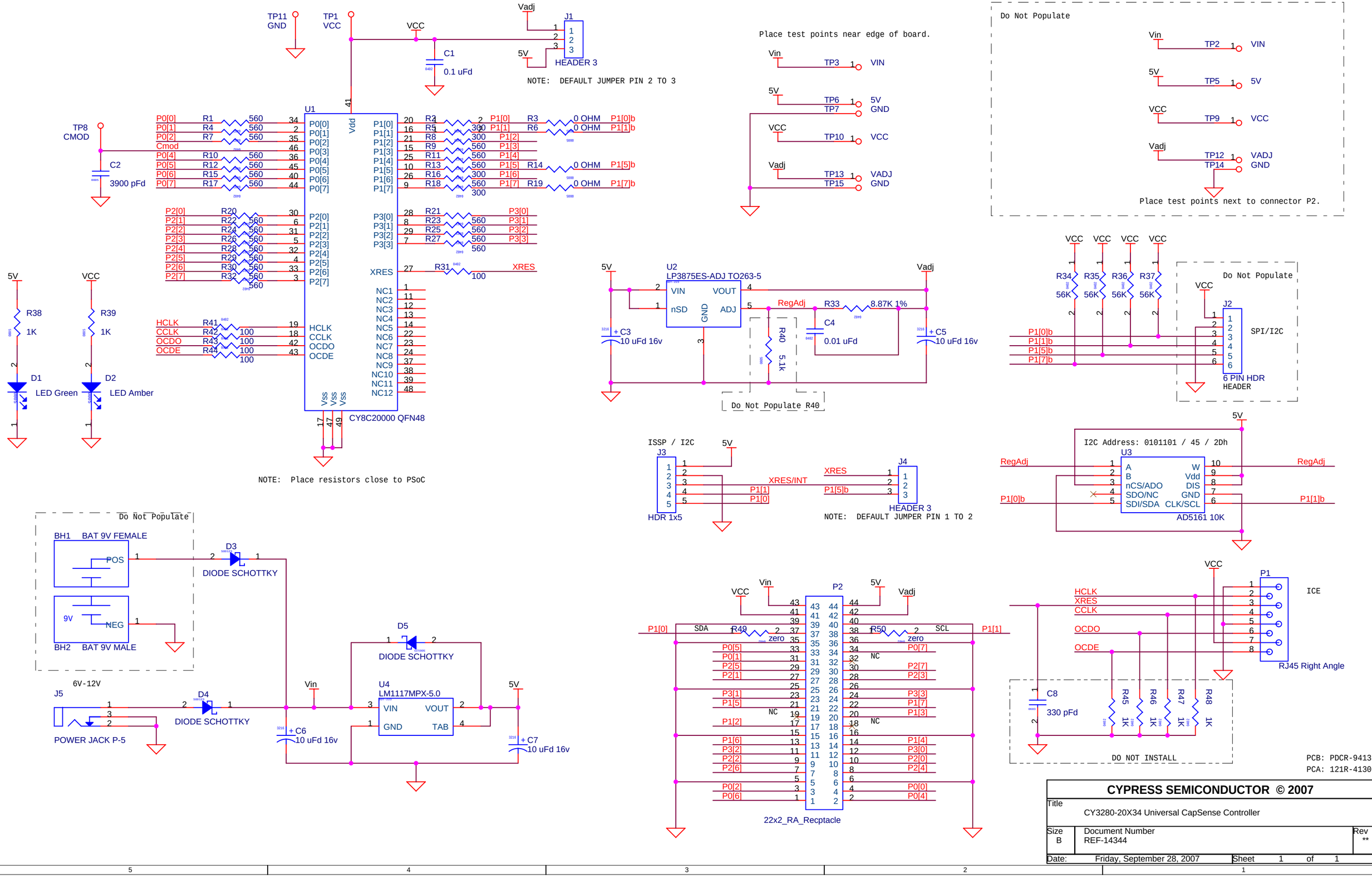

TP11 GND
TP1 VCC
VCC
C1 0.1 uFd
Vadj
J1
5V
HEADER 3
NOTE: DEFAULT JUMPER PIN 2 TO 3
U1
CY8C20000 QFN48
TP8 CMOD
C2 3900 pFd
P0[0] P0[1] P0[2] Cmod P0[4] P0[5] P0[6] P0[7]
P2[0] P2[1] P2[2] P2[3] P2[4] P2[5] P2[6] P2[7]
HCLK CCLK OCDO OCDE
XRES
P1[0] P1[1] P1[2] P1[3] P1[4] P1[5] P1[6] P1[7]
P3[0] P3[1] P3[2] P3[3]
0 OHM
NC1 NC2 NC3 NC4 NC5 NC6 NC7 NC8 NC9 NC10 NC11 NC12
Vss Vss Vss
NOTE: Place resistors close to PSoC
Place test points near edge of board.
TP3 VIN
TP6 5V
TP7 GND
TP10 VCC
TP13 VADJ
TP15 GND
Do Not Populate
TP2 VIN
TP5 5V
TP9 VCC
TP12 VADJ
TP14 GND
Place test points next to connector P2.
R38 1K
R39 1K
D1 LED Green
D2 LED Amber
U2
LP3875ES-ADJ TO263-5
VIN VOUT nSD GND ADJ
RegAdj
R33 8.87K 1%
C4 0.01 uFd
R40 5.1K
C3 10 uFd 16v
C5 10 uFd 16v
Do Not Populate R40
R34 56K
R35 56K
R36 56K
R37 56K
Do Not Populate
J2
SPI/I2C
6 PIN HDR
HEADER
ISSP / I2C
J3
HDR 1x5
XRES/INT
J4
HEADER 3
NOTE: DEFAULT JUMPER PIN 1 TO 2
I2C Address: 0101101 / 45 / 2Dh
U3
AD5161 10K
A B nCS/ADO SDO/NC SDI/SDA W Vdd DIS GND CLK/SCL
Do Not Populate
BH1 BAT 9V FEMALE
BH2 BAT 9V MALE
D3 DIODE SCHOTTKY
D4 DIODE SCHOTTKY
D5 DIODE SCHOTTKY
J5 6V-12V
POWER JACK P-5
U4
LM1117MPX-5.0
VIN VOUT GND TAB
C6 10 uFd 16v
C7 10 uFd 16v
P2
22x2_RA_Recptacle
SDA SCL
zero
NC
P1
ICE
RJ45 Right Angle
C8 330 pFd
R45 1K
R46 1K
R47 1K
R48 1K
DO NOT INSTALL
PCB: PDCR-9413
PCA: 121R-41300
CYPRESS SEMICONDUCTOR © 2007
Title
CY3280-20X34 Universal CapSense Controller
Size B
Document Number REF-14344
Rev **
Date: Friday, September 28, 2007
Sheet 1 of 1